

AMENDMENTS TO THE CLAIMS

Please **CANCEL** claims 1-22 without prejudice or disclaimer.

Please **ADD** claims 23-33 as shown below.

The following is a complete list of all claims in this application.

1-22. (Currently Cancelled)

23. (Currently Added) A liquid crystal display comprising:

a plurality of gate lines extending in a row direction and transmitting scanning signals;

a plurality of data lines extending in a column direction and transmitting picture signals;

a plurality of common electrode line pairs extending in the row direction, each common

electrode line pair comprising first and second common electrode lines arranged between two neighboring gate lines;

a plurality of first and second pixels arranged alternately in the row direction, each of the first and second pixels including a switching element connected to a corresponding one of the gate lines and a corresponding one of the data lines, a liquid crystal capacitor and a storage capacitor connected to the switching element,

wherein the storage capacitors of the first pixels are connected to the first common electrode lines and the storage capacitors of the second pixels are connected to the second common electrode lines.

24. (Currently Added) The liquid crystal display of claim 23, wherein the first and second common electrode lines transmit first and second common voltages, respectively,

during a polarity of the picture signals is changed from negative to positive, the first and second common voltages maintain a low level when the switching elements is turned off and repeatedly swing between the low level and a high level thereafter, and

during a polarity of the picture signals is changed from positive to negative, the first and second common voltages maintain the high level when the switching element is turned off and repeatedly swing between the low level to the high level thereafter.

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25. (Currently Added) The liquid crystal display of claim 24, wherein the first and second common voltages have inverted waveforms.

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26. (Currently Added) The liquid crystal display of claim 25, wherein the first common voltage applied to each first common electrode line is generated by inverting the first common voltage applied to one of the first common electrode lines adjacent thereto and shifting the inverted first common voltage by a pulse width of the scanning signals, and

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the second common voltage applied to each second common electrode line is generated by inverting the second common voltage applied to one of the second common electrode line adjacent thereto and shifting the inverted second common voltage by a pulse width of the scanning signals.

27. (Currently Added) A liquid crystal display comprising:

a plurality of gate lines extending in a row direction and transmitting scanning signals;

a plurality of data lines extending in a column direction and transmitting picture signals;

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a plurality of common electrode lines extending in the column direction and transmitting common voltages, the common electrode lines and the data lines being alternately arranged; and

a plurality of pixels, each of the pixels including a switching element connected to one of the gate lines and one of the data lines, a liquid crystal capacitor and a storage capacitor connected to the switching element,

wherein the storage capacitors of the pixels is connected to the common electrode lines.

28. (Currently Added) The liquid crystal display of claim 27, wherein during a polarity of the picture signals is changed from negative to positive, the common voltages maintain a low level when the switching elements is turned off and repeatedly swing between the low level and a high level thereafter, and

during a polarity of the picture signals is changed from positive to negative, the common voltages maintain a high level during the switching element is turned off and swing between the low level and the high level thereafter.

29. (Currently Added) The liquid crystal display of claim 27, wherein the common voltages applied to the neighboring common electrode lines have inverted wave form.

30. (Currently Added) A liquid crystal display comprising:
a plurality of gate lines extending in a first direction and transmitting scanning signals;
a plurality of data lines extending in a second direction and transmitting picture signals;
a plurality of common electrode lines extending in the first direction and transmitting common voltages, the common electrode lines and the gate lines being alternately arranged; and

a plurality of pixels arranged in a matrix, each of the pixels including a switching element connected to a corresponding one of the gate lines and a corresponding one of the data lines, a liquid crystal capacitor and a storage capacitor connected to the switching element, wherein the storage capacitors of the pixels on the same row are alternately connected to neighboring common electrode lines.

31. (Currently Added) The liquid crystal display of claim 30, wherein during a polarity of the picture signals is changed from negative to positive, the common voltage maintain a low level when the switching elements is turned off and repeatedly swings between the low level and a high level thereafter, and

during a polarity of the picture signals is changed from positive to negative, the common voltage maintain the high level when the switching element is turned off and repeatedly swings between the low level and the high level thereafter.

32. (Currently Added) The liquid crystal display of claim 30, wherein the common voltage applied to each common electrode line is generated by inverting the common voltage applied to one of the common electrode line adjacent thereto and shifting the inverted common voltage by a pulse width of the scanning signals.

33. (Currently Added) The liquid crystal display of claim 30, wherein each pixel includes two sub-pixels arranged in the second direction, and one of the two sub-pixels is connected to the common electrode line corresponding to the pixel.